

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

1-16. (cancelled)

17. (currently amended) A communication system including a differential signal transmitter, the transmitter comprising:

- a DAC decoder circuit, the DAC decoder circuit outputting DAC control words corresponding to digital input signals;[[;]]

- a differential current mode driver cell array;

- a selection circuit, the selection circuit asserting control signals in operative response to DAC control words, the selection circuit placing individual cells of the current driver cell array into a first operational mode sensitive to a first metric or into a second operational mode sensitive to a second metric in response to a select signal.

18. (original) The communication system according to claim 17, wherein the first metric corresponds to radiative emissions and wherein the second metric corresponds to power consumption.

19. (original) The communication system according to claim 18, each differential current mode driver cell comprising:

- first and second current sources, each conducting an equal quanta of current;

- first and second differential pairs, each pair coupled to a respective current source;

- a pair of differential outputs, a first output connected to a first transistor comprising each of the differential pairs, a second output connected to a second transistor comprising each of the differential pairs; and

- four control signal inputs, each input controlling to a respective one of the transistors comprising the first and second differential pairs.

20. (original) The communication system according to claim 19, further comprising:
a first logic circuit connected to receive the DAC control word, the first logic circuit asserting control signals which operate a corresponding signal component output circuit in the first mode; and
a second logic circuit connected to receive the control word, the second logic circuit asserting control signals which operate a corresponding signal component output circuit in the second mode.
21. (original) The communication system according to claim 20, each differential current mode driver cell comprising:
first and second current sources, each conducting an equal quanta of current;
first and second differential pairs, each pair coupled to a respective current source;
a pair of differential outputs, a first output connected to a first transistor comprising each of the differential pairs, a second output connected to a second transistor comprising each of the differential pairs; and
a set of control signal inputs, each input of the set controlling a respective one of the transistors comprising the first and second differential pairs.
22. (original) The communication system according to claim 21, the first and second logic circuits each defining control signals in response to a DAC control word, said first and second differential pairs operatively responsive to said control signals to output a differential signal in either the first mode or the second mode.
23. (original) The communication system according to claim 22, wherein the DAC control word is the same when the first and second differential pairs output a differential signal in either the first mode or the second mode.
24. (original) The communication system according to claim 23, wherein the first mode is a Class-A mode and wherein the second mode is a Class-B mode.
25. (cancelled)